

Onnea ☺

**Question 1.** Fig. 1 shows an ideal PWM shunt regulator widely applied in space application. The MOSFET is turned on during the off-time. Solve the transfer functions representing the dynamics of the converter according to

$$\begin{bmatrix} \hat{v}_{in} \\ \hat{i}_o \end{bmatrix} = \begin{bmatrix} Z_{in-o} & T_{oi-o} & G_{ci-o} \\ G_{io-o} & -Y_{o-o} & G_{co-o} \end{bmatrix} \begin{bmatrix} \hat{i}_{in} \\ \hat{v}_o \\ \hat{d} \end{bmatrix} \quad (1)$$

Each correct transfer function will give 1 pt.

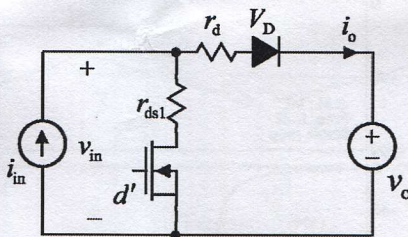


Fig. 1 Ideal PWM shunt regulator.

**Question 2.** Two different control-to-output-voltage transfer functions are of the form

$$G_{co}^1 = K \frac{1 - s / \omega_z}{(1 + s / \omega_{p1})(1 + s / \omega_{p2})} \quad (2)$$

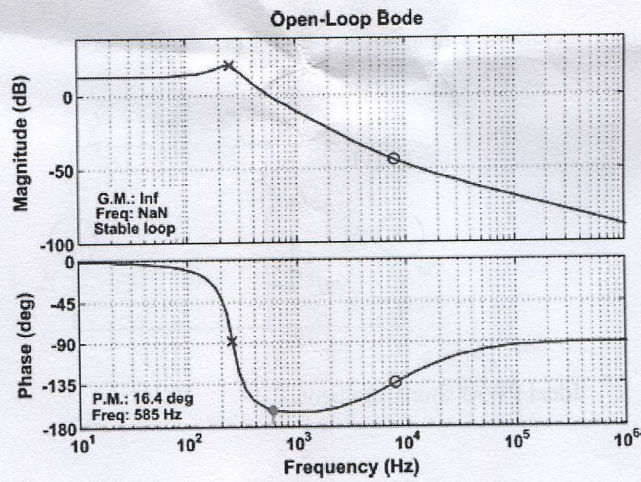
and

$$G_{co}^2 = K \frac{1 + s / \omega_z}{(1 - s / \omega_{p1})(1 + s / \omega_{p2})} \quad (3)$$

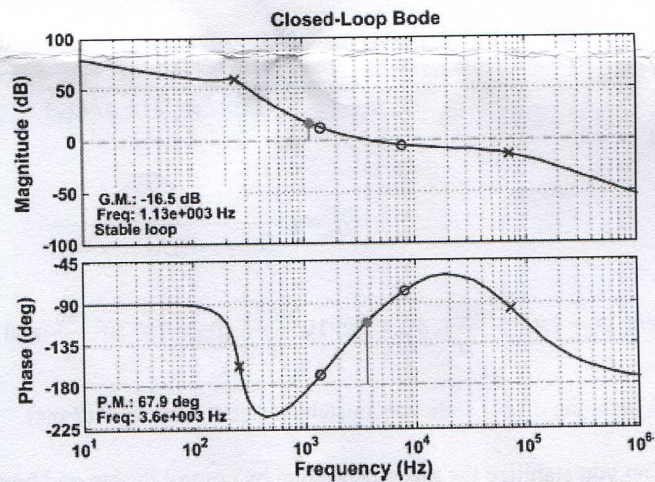
where  $\omega_z = 2\pi \cdot 10^4 \frac{\text{rad}}{\text{s}}$ ,  $\omega_{p1} = 2\pi \cdot 10^3 \frac{\text{rad}}{\text{s}}$ ,  $\omega_{p2} = 2\pi \cdot 10^5 \frac{\text{rad}}{\text{s}}$  and  $K = 10$ .

- Which one of  $G_{co}^i$  indicates unstable system and why? (2 pts)
- Can you stabilize the unstable system by control design and how? (2 pts)
- Does the stable system contain any control design limitation? Justify your answer (2 pts).

Question 3. Fig. 3 shows  $G_{co-o}$  (a) and the corresponding final loop gain  $L_{out}$  (b). a) What type of controller is used in the design (P, I, PI, or PID). Justify your answer (2 pts). b) Is the closed-loop converter stable? Justify your answer (1 pts), and c) What kind of controller would suit better for this purpose and how its zeros and poles should be placed (3 pts).



(a)



(b)

Fig. 3 Buck converter: (a)  $G_{co-o}$ , and (b)  $L_{out}$

**Question 4.** Fig. 4 shows the measured input impedances of a buck converter equipped with an input LC filter. The output impedance of the LC filter in Fig. 4 shows the typical frequency-domain behaviour of a parallel resonant circuit.

- Estimate the output-voltage-loop crossover frequency based on the frequency-domain behaviour of the closed-loop and open-loop input impedances in Fig. 4 (2pts).
- Is the converter-LC-filter system stable? Justify your answer based on the impedance behaviour in Fig. 4 (2 pts).
- What is the approximate value of the converter ideal input impedance ( $Z_{in-\infty}$ ) according to Fig. 4. Justify your answer (2 pts).

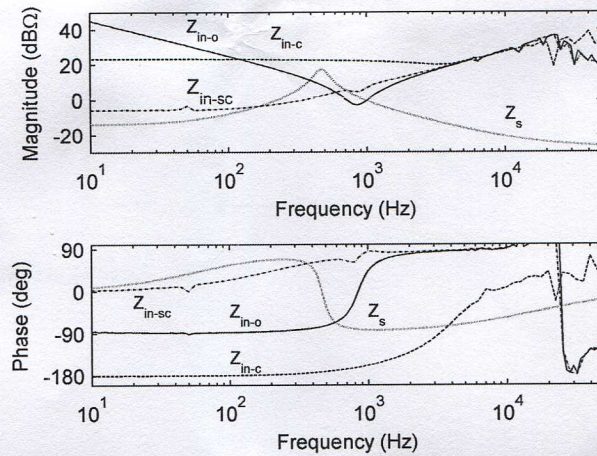


Fig. 4 The measured input impedances of a buck converter.

**Question 5.** Fig. 5 shows the typical input filter structure of the voltage-fed converters, which is characterized by four transfer functions. Compute those transfer functions by utilizing only circuit theory. Each correct transfer functions will give 1 pts and the identification of the correct set would give 2 pts. Only the named method would give the points. If other techniques are used then the outcome would be zero.

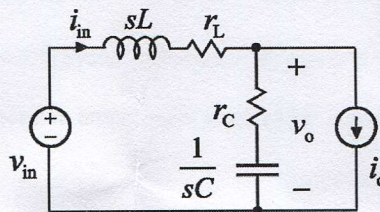


Fig. 5 Input LC filter